

Figure 1

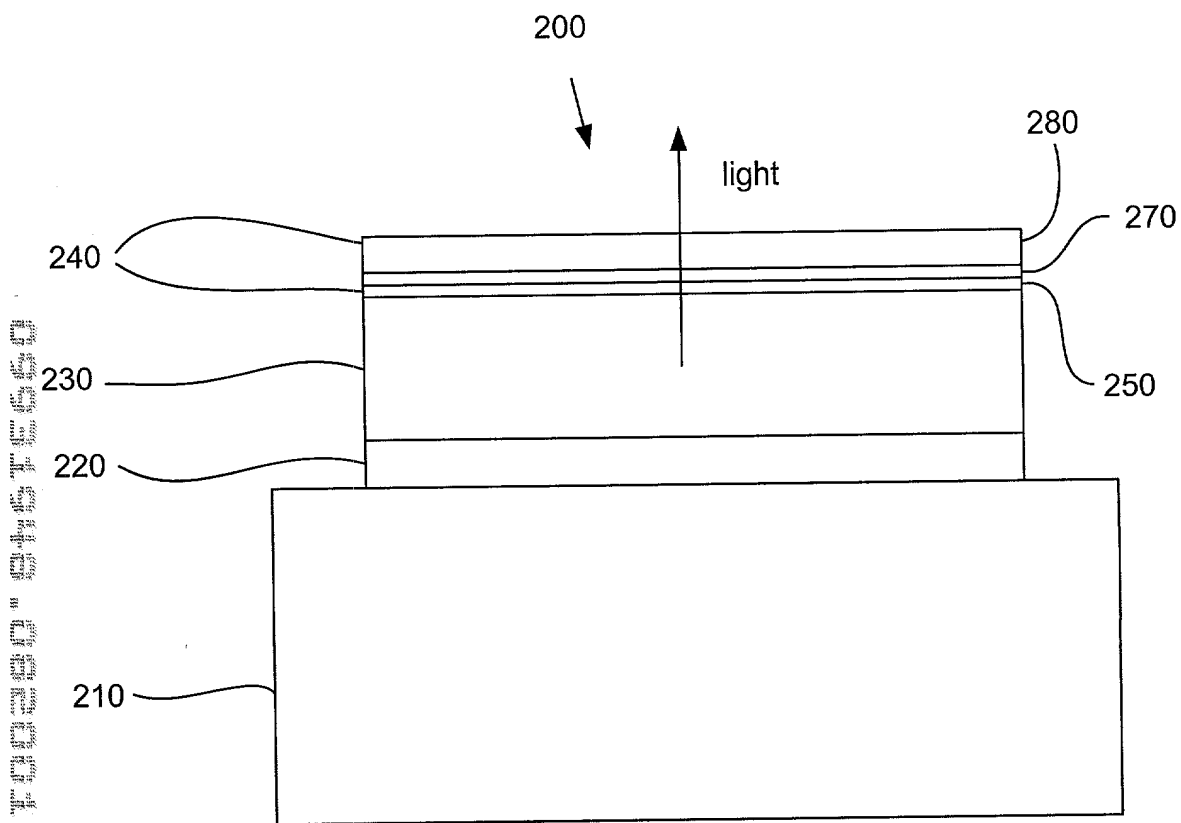


Figure 2

Figure 3 is a schematic diagram of a device 300. The device 300 includes a substrate 310, a first layer 320, a second layer 330, a third layer 340, a fourth layer 350, and a fifth layer 360. A light source 380 is positioned above the device 300, and light is directed towards the device 300.

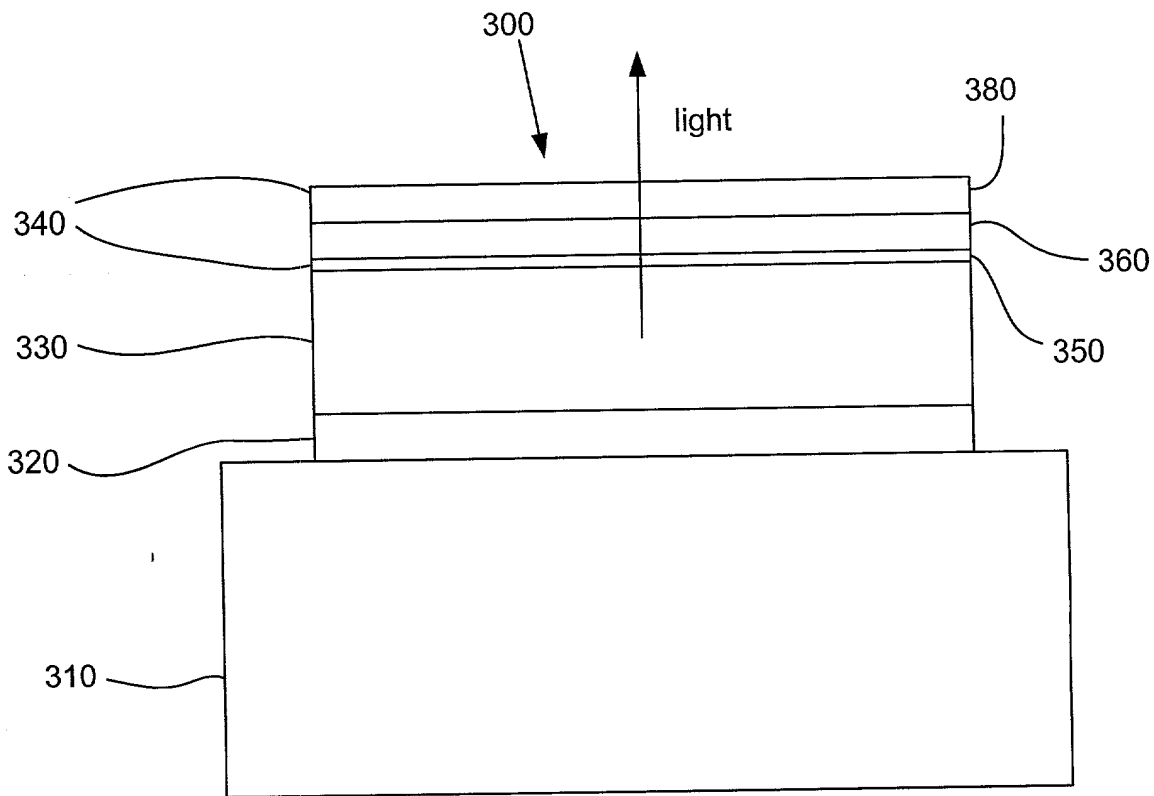


Figure 3

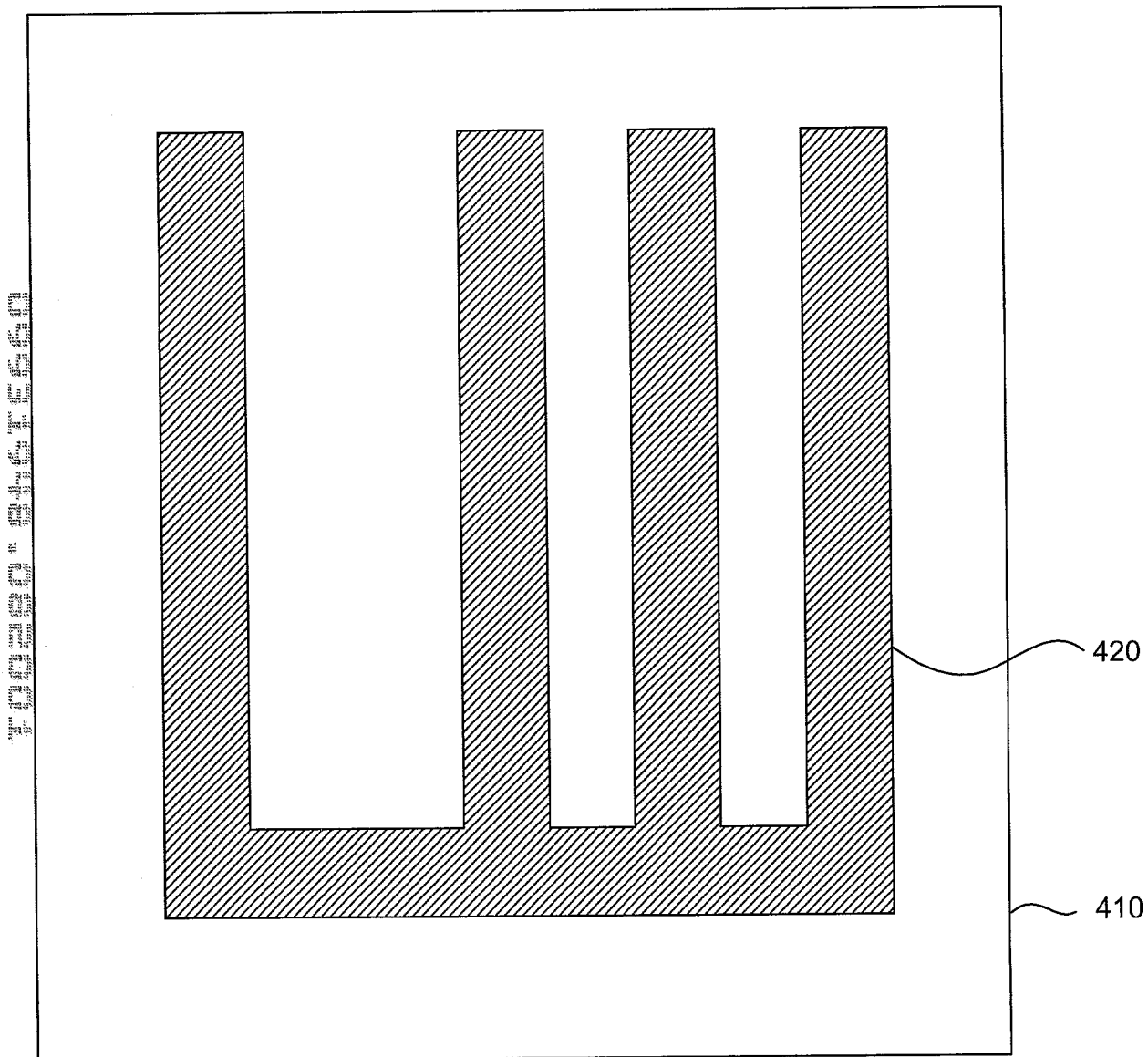
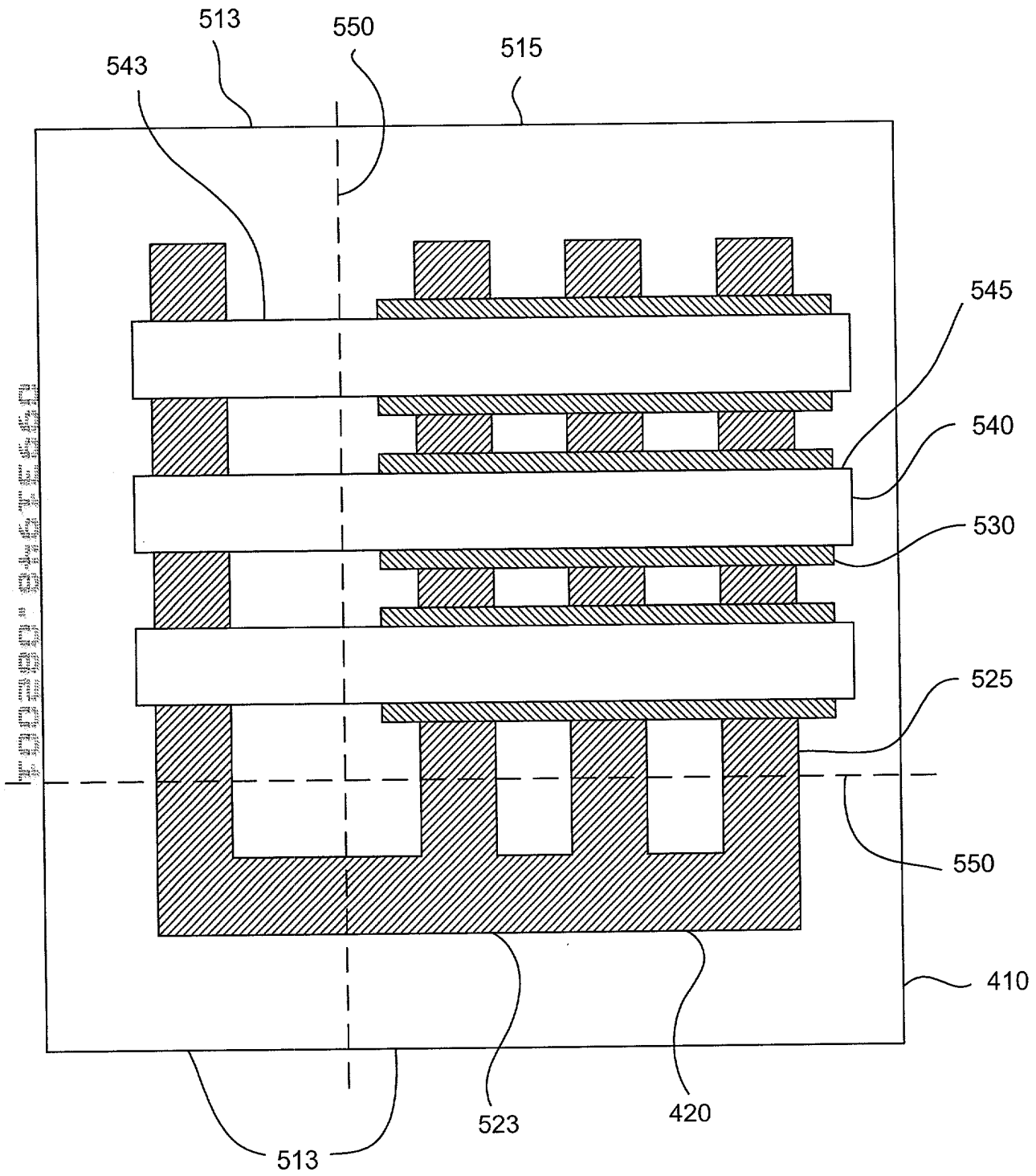


Figure 4

Figure 5



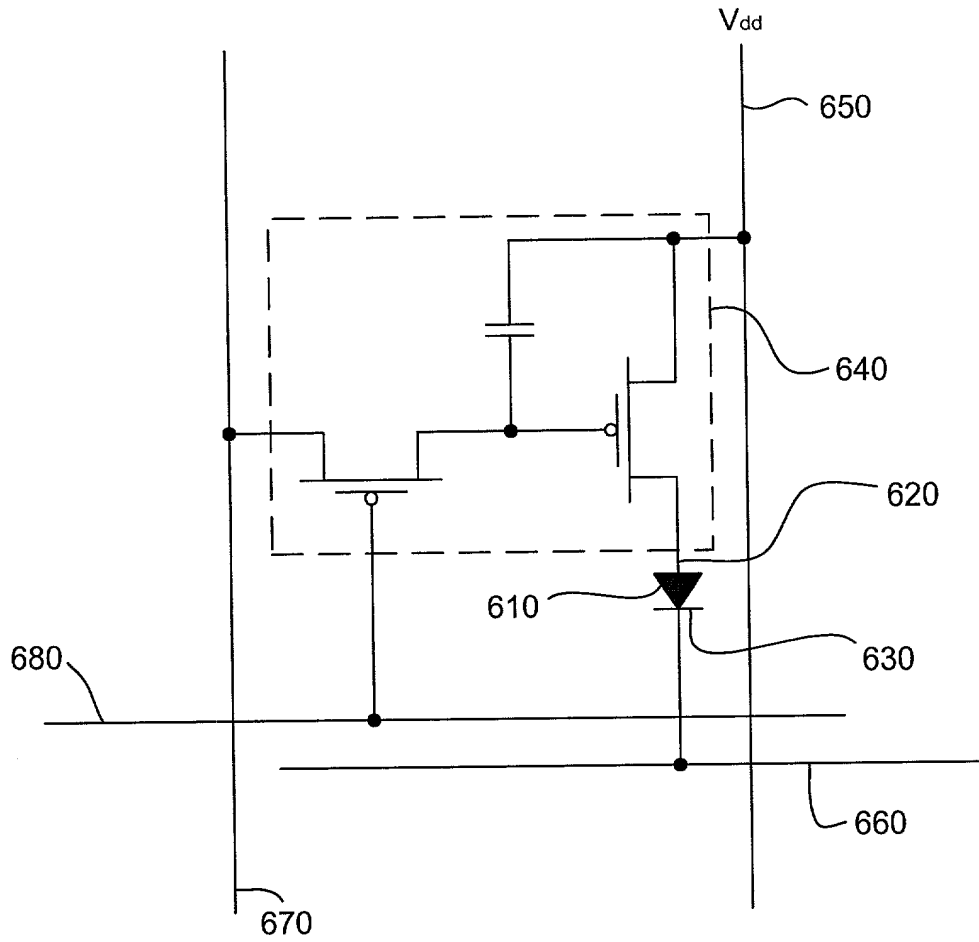


Figure 6

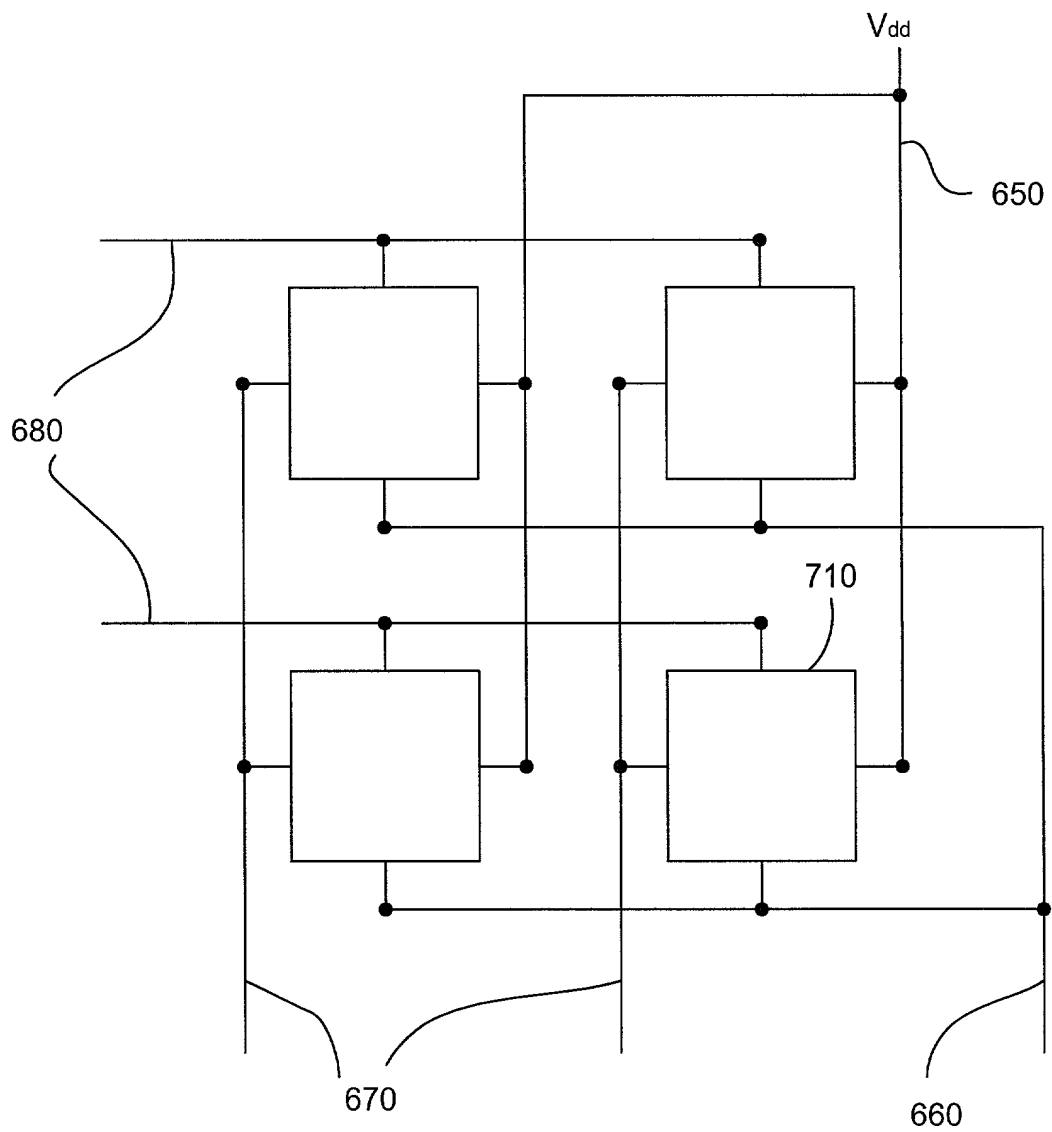


Figure 7

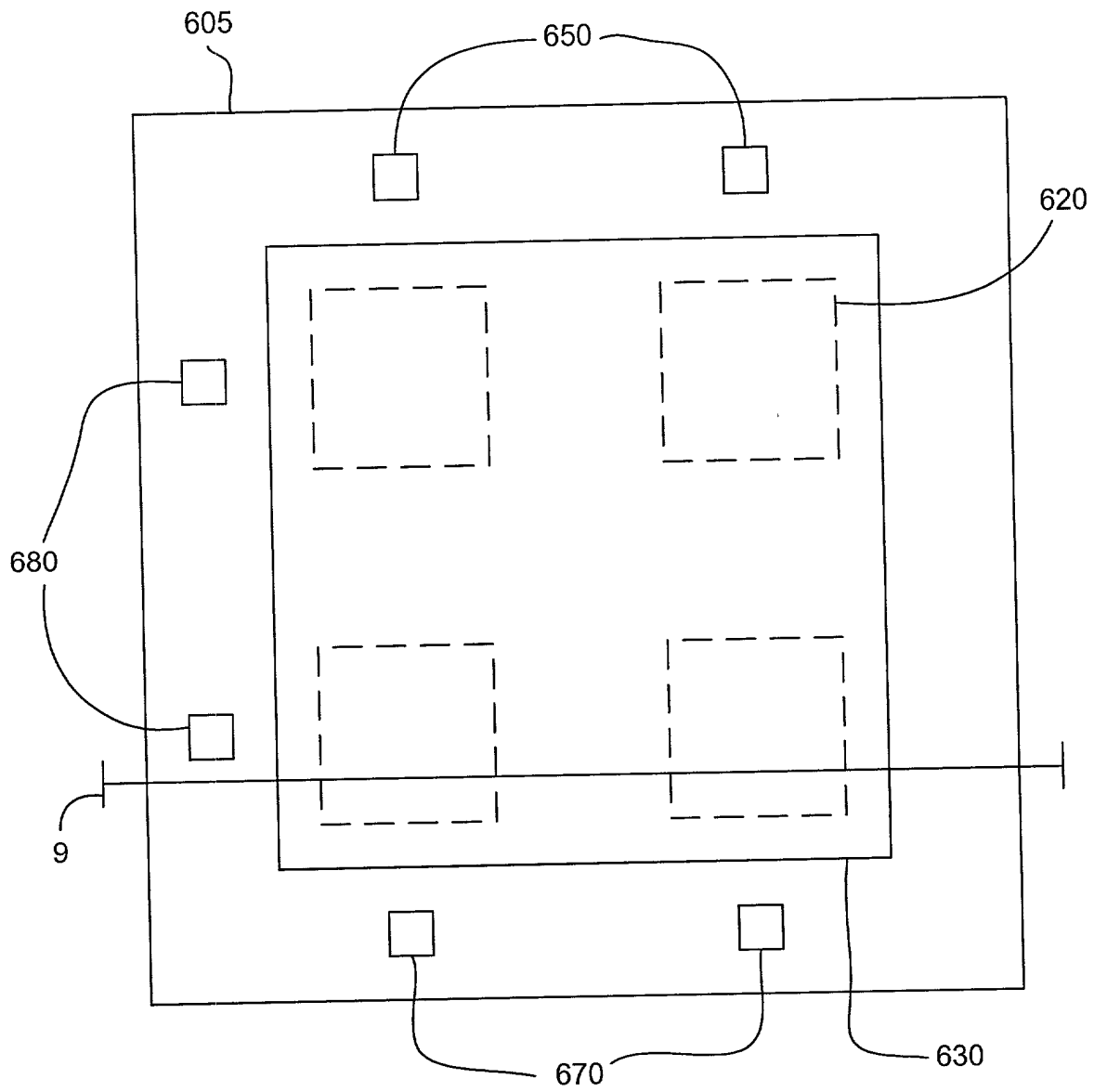


Figure 8

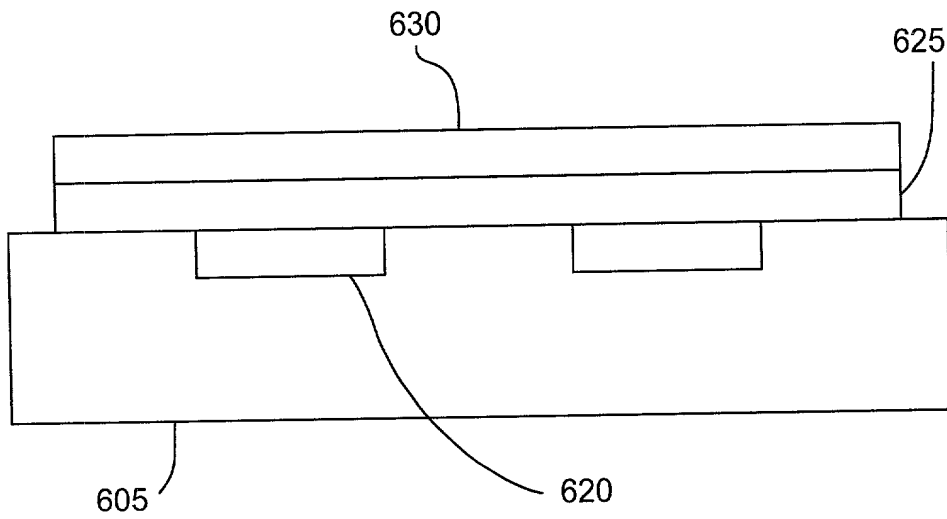


Figure 9

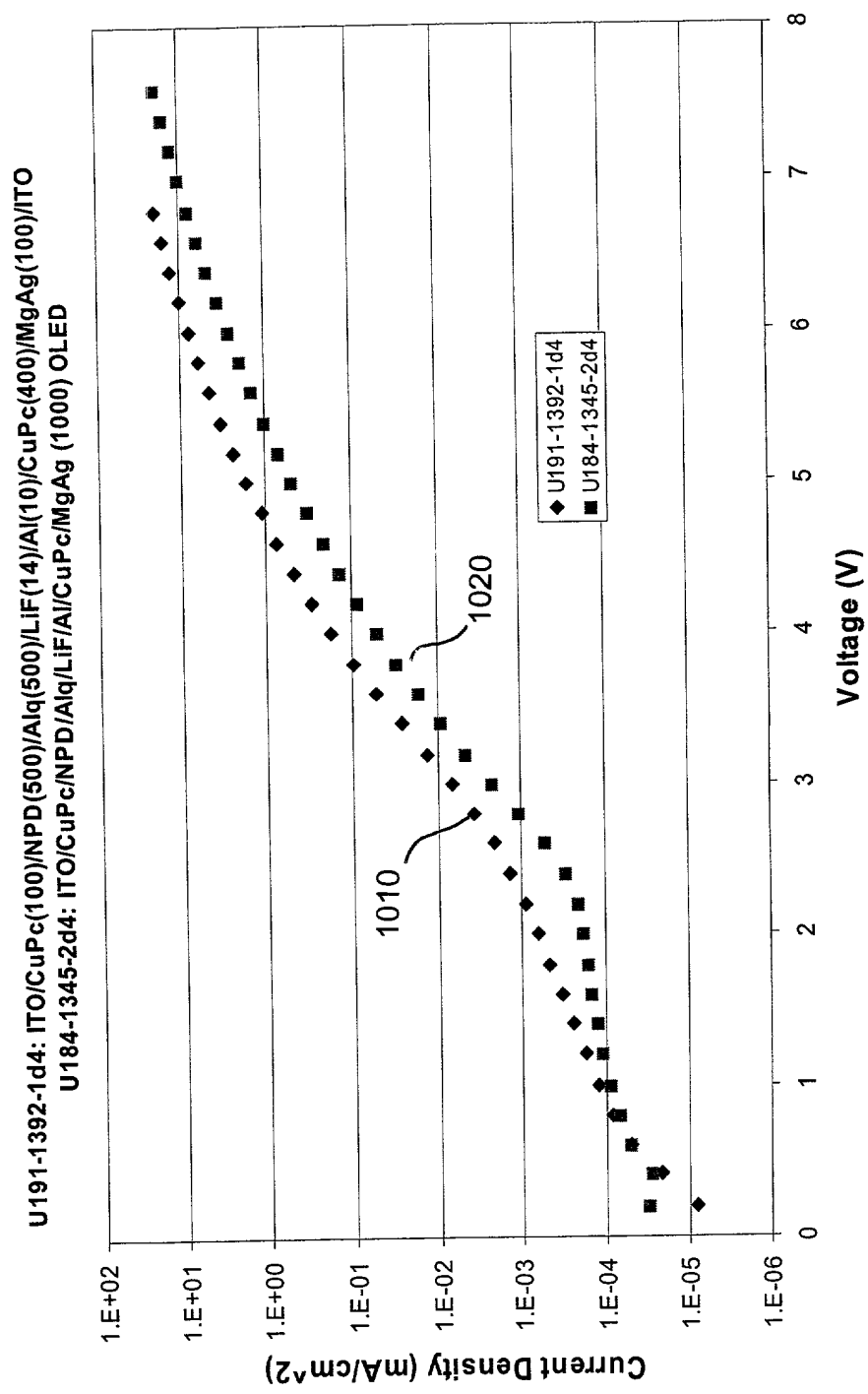


Figure 10

U131-856-9d2 undoped TOLED MgAg/ITO cathode 13.7 mA/cm² 600 nits
U191-1392-9d4 undoped TOLED LiF/Al/CuPc/MgAg/ITO cathode 10 mA/cm²

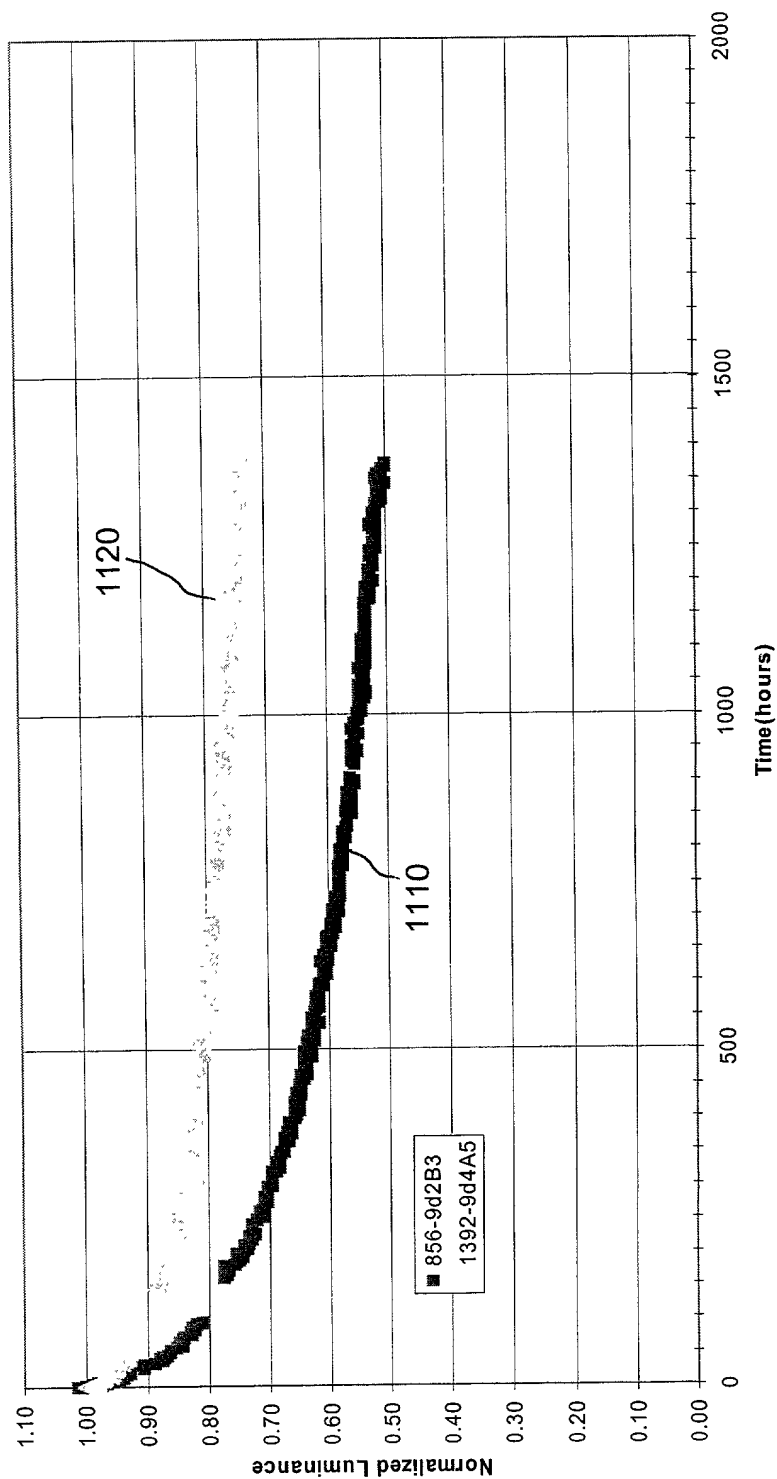


Figure 11